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NO. 2404 P. 5/17

Docket No.: 21806-00158-US
(PATENT)**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:
Kiran V. Chatty et al.**Application No.: 10/709,041****Confirmation No.: 3040****Filed: April 8, 2004****Art Unit: 2815****For: LOW TRIGGER VOLTAGE ESD NMOSFET**
TRIPLE-WELL CMOS DEVICES

Examiner: M. E. Warren**AMENDMENT****MS Amendment**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**Dear Sir:**

In response to the Office Action mailed November 29, 2005, please amend the above-identified U.S. patent application as follows:

AMENDMENTS TO THE CLAIMS begin on page 2.

REMARKS begin on page 6.